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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|---|-------------|----------------------|---------------------------|------------------|
| 10/707,416 | 12/12/2003 | Yu-Chi Lee | CPTP0001USA | 1415 |
| 27765 | 7590 | 05/18/2005 | EXAMINER | |
| NORTH AMERICA INTERNATIONAL PATENT OFFICE (NAIPC) | | | CHOWDHURY, TARIFUR RASHID | |
| P.O. BOX 506 | | | ART UNIT | PAPER NUMBER |
| MERRIFIELD, VA 22116 | | | 2871 | |

DATE MAILED: 05/18/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Ex

| | | | |
|------------------------------|---|----------------------------------|--|
| Office Action Summary | Application No. 10/707,416 | Applicant(s) LEE ET AL | |
| | Examiner Tarifur R. Chowdhury | Art Unit 2871 | |

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-4, 8-14 and 17-21 is/are rejected.
- 7) ☒ Claim(s) 5-7, 15 and 16 is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 12 December 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date ____. | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Specification

2. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claim 11 is rejected under 35 U.S.C. 102(b) as being anticipated by Yayoi

Nakamura (Yayoi), JP 10-123544 or Yoshiko Mino (Yoshiko), JP 03-015822.or

Watanabe et al., (Watanabe), US 2002/0167634.

5. Yayoi shows in Fig. 1, Yoshiko shows in Fig. 1(d) and Watanabe shows in Fig. 6, a display panel comprising:

- a first substrate comprising:
- a pixel array region positioned on a central part of the first substrate;

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- a sealant region positioned on a bordering part of the first substrate and surrounding the pixel region; and
 - a light-shielding pattern positioned on a surface of the sealant region;
 - a second substrate;
 - a sealant pattern positioned between the first substrate and the second substrate and corresponding to the sealant region for combining the first substrate with the second substrate; and
- a liquid crystal layer positioned between the first substrate and the second substrate.

Accordingly, claim 11 is anticipated.

6. Claims 1, 2, 8-12 and 18-21 are rejected under 35 U.S.C. 102(e) as being anticipated by Lee et al., (Lee), US 2004/0183992.

7. The applied reference has common inventors with the instant application. Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 102(e) might be overcome either by a showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not the invention "by another," or by an appropriate showing under 37 CFR 1.131.

8. Lee discloses (page 1, paragraph 0010; page 2, paragraph's 0021-0024) and shows in Fig. 1, a one drop fill (ODF) liquid crystal display (LCD) panel comprising::

- a first substrate (150) comprising:

- a pixel array region (not shown) positioned on a central part of the first substrate (150);
- a sealant region positioned on a bordering part of the first substrate and surrounding the pixel region; and
- a metal light-shielding pattern (140) positioned on a surface of the sealant region;

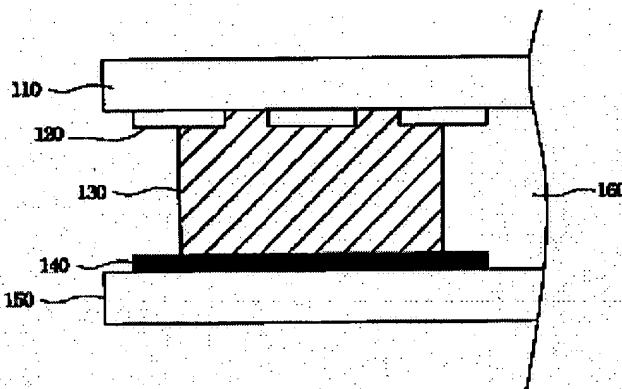


FIG. 1

- a second substrate (110);
- a sealant pattern (130) positioned between the first substrate and the second substrate and corresponding to the sealant region for combining the first substrate with the second substrate; and
- a liquid crystal layer (160) positioned between the first substrate and the second substrate.

Accordingly, claims 1, 8-11 and 18-21 are anticipated.

As to claims 2 and 12, Lee also discloses that the sealant pattern (130) is UV type (page 2, paragraph 0022) (applicant's photocuring sealant).

9. Claims 1-4, 8-14 and 18-21 are rejected under 35 U.S.C. 102(e) as being anticipated by Chu et al., (Chu), US 2005/0062910.

10. The applied reference has a common inventor with the instant application.

Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 102(e) might be overcome either by a showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not the invention "by another," or by an appropriate showing under 37 CFR 1.131.

11. Chu discloses and shows in Fig. 4, a one drop fill (ODF) liquid crystal display (LCD) panel comprising:

- a first substrate (202) comprising:
- a pixel array region (214) positioned on a central part of the first substrate;

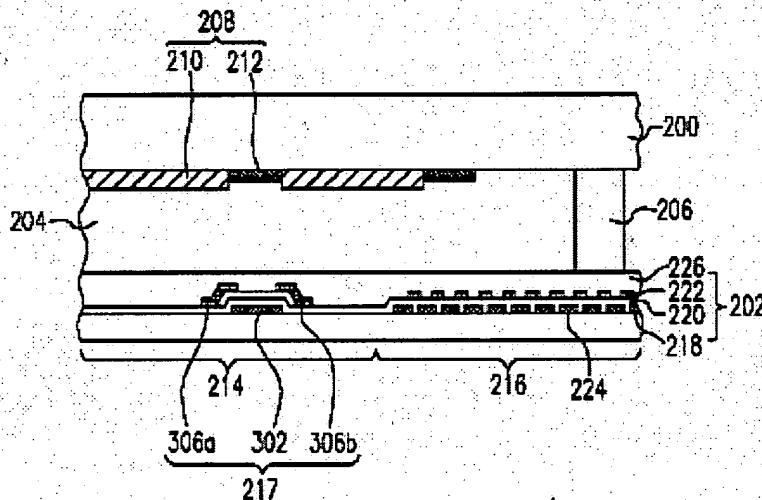


FIG. 4

- a sealant region (216) positioned on a bordering part of the first substrate and surrounding the pixel region; and

- light-shielding patterns (222, 224) positioned on a surface of the sealant region (216)
- a second substrate (200);
- a sealant pattern (206) positioned between the first substrate and the second substrate and corresponding to the sealant region for combining the first substrate with the second substrate; and
- a liquid crystal layer (204) positioned between the first substrate and the second substrate.

Accordingly, claims 1, 8-11 and 18-21 are anticipated.

As to claims 2 and 12, Chu also discloses that the sealant pattern (206) is fabricated from an UV photosensitive material (page 3, paragraph 0036) (applicant's photocuring sealant).

As to claims 3, 4, 13, 14 and 17, Chu also discloses and shows in Fig. 4, that the light shielding pattern is for preventing light leakage through the sealant region (216), the light-shielding pattern comprising a first metal pattern (222), a second metal pattern (224), and a first insulation layer (220) positioned between the first metal pattern and the second metal pattern (Page 3, paragraph 0035) and that the pixel array region (214) further comprises a plurality of pixels arranged in arrays, each pixel including a thin film transistor (217).

Allowable Subject Matter

12. Claims 5-7, 15 and 16 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

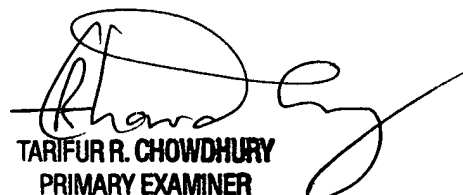
Conclusion

13. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tarifur R. Chowdhury whose telephone number is (571) 272-2287. The examiner can normally be reached on M-Th (6:30-5:00) Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571) 272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TRC
May 06, 2005


TARIFUR R. CHOWDHURY
PRIMARY EXAMINER